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	First Named Inventor	Edlin Solomon	
	Art Unit		
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References

1. Jung Jong-Wan. Method for fabricating semiconductor device with copper gate electrode. U.S. Patent No. 6479357. H01L 021/336. Priority Feb 29, 2000.
2. Edlin S.D. JFET transistor and method for manufacturing the same. R.F. Patent No. 2102818. H01L 29/80. Priority Apr. 15. 1992.
3. Edlin S.D. The application for issue of the patent of RF No. 2000100080. A bipolar static induction transistor. H01L 29/06. Application mailed 01.05.2000.
4. Smoliansky B.A. et al. Author's certificate USSR No. 736807. H01L 29/70. Priority 01.22. 1979.
5. Aizawa Yoshiaki et al. Semiconductor device. JP Patent No. 3352840. H01L 29/78. H03K 17/56. H03K 17/68. Priority 14.03. 1994.
6. Nishizawa Jun-ichi. Static induction type semiconductor device with multiple doped layers for potential modification. U.S. Patent No. 4364072. H01L 029/80. Priority Mar 17, 1978.